

## **Application Data Sheet**

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### **Inventor Information**

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### **Correspondence Information**

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### **Application Information**

Title Line One:	METHOD OF MANUFACTURING INSULATED GATE SEMICONDUCTOR DEVICE (AS AMENDED)
Total Drawing Sheets:	3
Formal Drawings?:	YES
Application Type:	Division
Docket Number:	492322001810

### **Representative Information**

Representative Customer Number:	25227
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### **Continuity Information**

This application is a:	Division
> Application One:	09/925,628
Filing Date:	August 10, 2001

### **Prior Foreign Applications**

Foreign Application One:	2000-242617
Filing Date:	August 10, 2000
Country:	Japan
Priority Claimed:	Yes

va-50654  
Initial 11/25/2003

## **Assigne Information**

Assignee name::	Sanyo Electric Co., Ltd.
Street of mailing address::	2-5-5, Keiyanhondori
City of mailing address::	Moriguchi-City
State of mailing address::	Osaka
Country of mailing address::	Japan
Postal or Zip Code of mailing address::	570-8677

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